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Substitute for Form 1449A/PTO (Modified) INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/024,119
				Filing Date	December 17, 2000
				First Named Inventor:	Yuffe et al.
				Group Art Unit	Not yet assigned
				Examiner Name	Not yet assigned
Sheet	2	Of	2	Attorney Docket Number	42P12370

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OTHER ART – NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
JK		Kudoh J et al.: "A CMOS GATE ARRAY WITH DYNAMIC-TERMINATION GTL I/O CIRCUITS" International Conference on Computer Design: VLSI in Computers and Processors. Austin, Oct. 2-3, 1995, New York, IEEE, US	
JK		International Search Report April 15, 2003	

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Examiner Signature		Date Considered	8-8-03
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